



TFT MODULE SPECIFICATION

RVT70HSLFWN00 V2

IPS LVDS 7.0" LCD TFT DATASHEET
Rev. 1.0
2026-07-17

Riverdi Sp. z o.o.

Nowy Swiat 36
80-299 Gdansk, Poland
VAT ID: PL5842767153
Registration number (KRS): 0000923967

+48 587 703 116

Office hours: 9:00 to 17:30 CET (Mon-Fri)

contact@riverdi.com

riverdi.com

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally black/IPS	/
Size	7.0	Inch
Viewing Direction	Free	/
Outside Dimensions (W x H x D)	181.60 x 100.60 x 6.39	mm
Active Area (W x H)	154.21 x 85.92	mm
Pixel Pitch (W x H)	0.1506 x 0.1432	mm
Resolution	1024 (RGB) x 600	/
Brightness	1000	cd/m ²
LCD Interface Type	LVDS	/
Color Depth	16.7 M	/
Pixel Arrangement	RGB Vertical Stripe	/
LCD Driver	EK79001HK+EK73215BCGA	/
With/Without Touch	Without Touch Panel	/
LCD Input Voltage	3.3	V
Weight	TBD	g

Note 1. RoHS3 compliant

Note 2. LCM weight tolerance: ± 5%.

1. REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2026-07-17	Initial Release	

2. CONTENTS

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3. ASSEMBLY

3.1 Mounting Frame

For dimensions 7.0", 10.1", 12.1", and 15.6" the product with mounting frame version is available. Thanks to the four catches attached to the side, frame provides strong assembly to the surface by mounting element (like the screw, see Figure 1). The frames are specially designed to fit Riverdi products perfectly. The diameter of the mounting hole is 3.5mm.

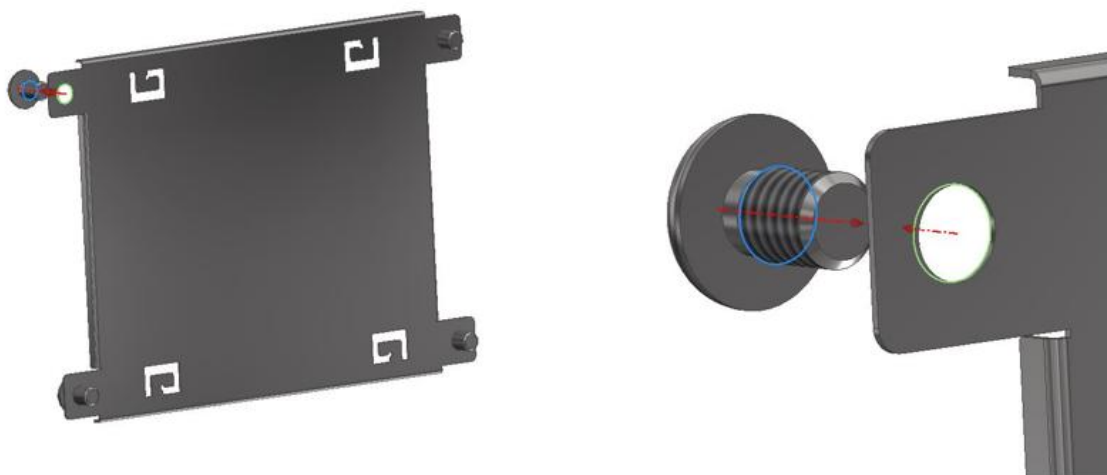
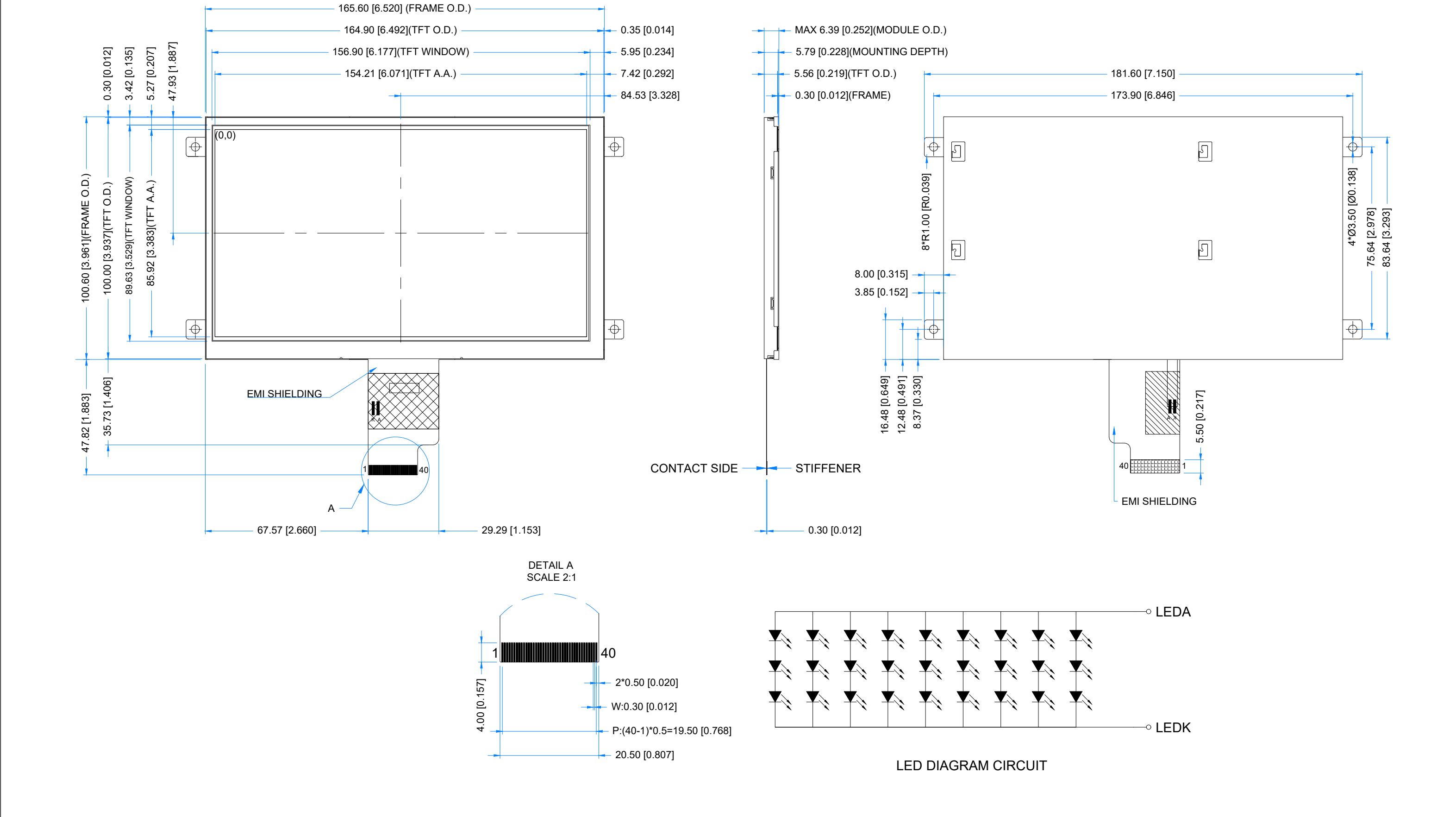


Figure 1. Mounting Frame

4. MODULE CLASSIFICATION INFORMATION

RV	T	70	H	S	L	F	W	N	00	V2
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.	11.

NO.	PARAMETER	SYMBOL
1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard
3.	DISPLAY SIZE	70 – 7.0”
4.	MODEL SERIAL NO.	H – High Brightness, IPS
5.	RESOLUTION	S – 1024 x 600 px
6.	INTERFACE	L – TFT LCD, LVDS
7.	FRAME	F – With Mounting Metal Frame
8.	BACKLIGHT TYPE	W – LED White
9.	TOUCH PANEL	N – Without Touch Panel
10.	VERSION	00 – version
11.	MODULE REVISION	V2



6. ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT	NOTES
Power Voltage for LCM	VDD	-0.3	3.6		-
Backlight forward current	I _F	-	30	mA	For each serial LED
Operating Temperature	T _{OP}	-20	70	°C	
Storage Temperature	T _{ST}	-30	80	°C	
Module supply	AVDD	-	9.5	V	
	VCOM	3.0	4.0	V	
	VGH	17	19	V	
	VGL	-6.5	-5.5	V	

Note. The above are maximum values. If exceeded, they may cause permanent damage to the unit.

7. ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	REMARK	
Supply Voltage for Module	VDD	3.0	3.3	3.6	V	Ta=25°C	
	AVDD	-	9.4	9.5			
	VGH	17	18	19			
	VGL	-6.5	-6.0	-5.5			
Current consumption	I _{VDD}	-	25	37.5	mA		Ta=25°C
	I _{AVDD}	-	25	37.5			
	I _{VGH}	-	0.37	0.56			
	I _{VGL}	-	0.5	0.8			
	I _{VCOM}	-	0.05	0.1			

8. BACKLIGHT ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Backlight Driving Voltage	V_F	8.4	9.0	10.2	V	
Backlight Driving Current	I_F	-	270	-	mA	
Backlight Power Consumption	W_{BL}	-	2.43	-	W	
Backlight Lifetime	-	-	50,000	-	hours	Note

Note: The “Operating lifetime” is defined as the module brightness decrease to 50% original brightness at $T_a=25\text{ }^{\circ}\text{C}$ and $I_F=270\text{ mA}$. The LED lifetime could be decreased if operating I_F is larger than 270 mA. Typical operating lifetime is estimated data.

9. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	RMK	NOTE
Response Time	$Tr+Tf$	$\theta=0^{\circ}$ $\phi=0^{\circ}$ $T_a=25\text{ }^{\circ}\text{C}$	-	28	45	ms	FIG 2.	4, 7
Contrast Ratio	Cr		600	800	-	---	FIG 3.	1, 7
Surface Luminance	L_v		-	1000	-	cd/m^2		2, 7
Uniformity	U	-	70	75	-	%	FIG 3.	3
Viewing Angle Range	θ	$\phi = 90^{\circ}$	80	85	-	deg	FIG 4.	6
		$\phi = 270^{\circ}$	80	85	-	deg		
		$\phi = 0^{\circ}$	80	85	-	deg		
		$\phi = 180^{\circ}$	80	85	-	Deg		
CIE (x, y) Chromaticity	Rx	$\theta=0^{\circ}$ $\phi=0^{\circ}$ $T_a=25\text{ }^{\circ}\text{C}$	0.557	0.587	0.617	-	FIG 3.	5,7
	Ry		0.313	0.343	0.373	-		
	Gx		0.323	0.353	0.383	-		
	Gy		0.552	0.582	0.612	-		

	Bx		0.124	0.154	0.184	-		
	By		0.091	0.121	0.151	-		
	Wx		0.274	0.304	0.334	-		
	Wy		0.300	0.330	0.360	-		
NTSC ratio	-		40	47	-	%		

Note 1. Contrast Ratio (CR) is defined mathematically as below, for more information see Figure 2.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 2.

$$L_v = \text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}$$

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the minimum luminance of 5 points luminance by maximum luminance of 5 points luminance. For more information see Figure 2.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, T_r) and from black to white (Decay Time, T_f). For additional information see Figure 1. The test equipment is BM-7A.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6. For TFT module, viewing angle is the angle at which the contrast ratio is greater 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see Figure 3.

Note 7. Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80). For response time testing, the testing data is based on BM-7A. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, Chromaticity the test data is based on SR-3A.

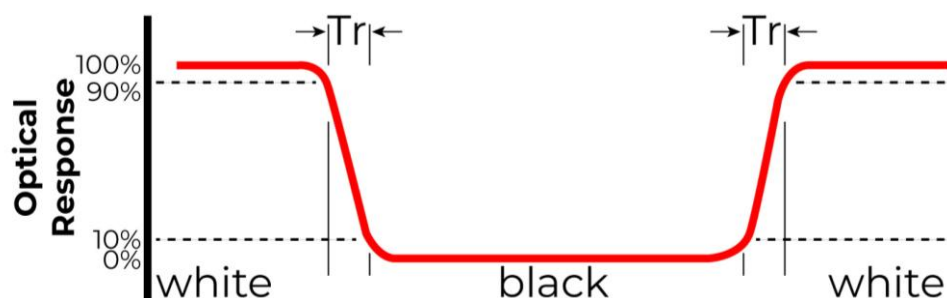


Figure 2. The definition of response time

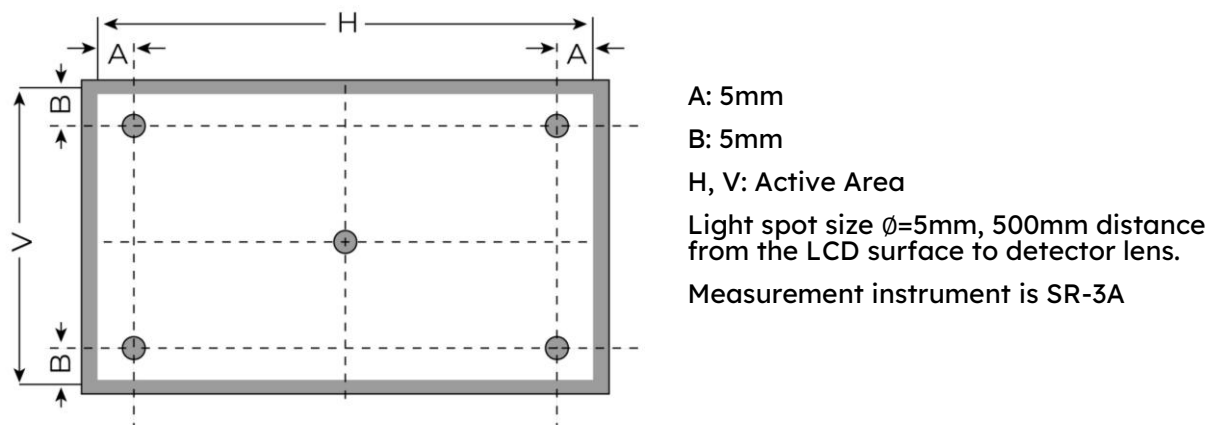


Figure 3. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

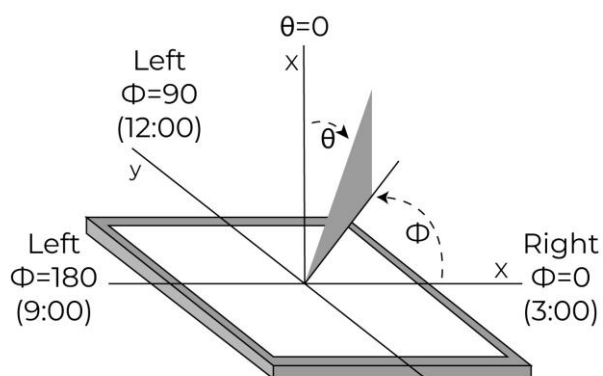


Figure 4. The definition of viewing angle

10. INTERFACES DESCRIPTION

10.1 TFT ASSIGNMENT

PIN	SYMBOL	I/O/P	DESCRIPTION
1	VCOM	P	Common voltage
2	VDD	P	Power voltage for digital circuit
3	VDD	P	Power voltage for digital circuit
4	NC	-	No connection
5	RESET	I	Global reset pin
6	STBYB	I	Standby mode, normally pulled high
7	GND	P	Ground
8	RXIN0-	I	-LVDS differential data input
9	RXIN0+	I	+LVDS differential data input
10	GND	P	Ground
11	RXIN1-	I	-LVDS differential data input
12	RXIN1+	I	+LVDS differential data input
13	GND	P	Ground
14	RXIN2-	I	-LVDS differential data input
15	RXIN2+	I	+LVDS differential data input
16	GND	P	Ground
17	RXCLKIN-	I	-LVDS differential clock input
18	RXCLKIN+	I	+LVDS differential clock input
19	GND	P	Ground
20	RXIN3-	I	-LVDS differential data input
21	RXIN3+	I	+LVDS differential data input

22	GND	P	Ground
23	NC	-	No connection
24	NC	-	No connection
25	GND	P	Ground
26	NC	-	No connection
27	NC	-	No connection
28	SELB	I	6-bit/8bit mode select
29	AVDD	P	Power for analog circuit
30	GND	P	Ground
31	LED-	P	LED cathode
32	LED-	P	LED cathode
33	L/R	I	Horizontal inversion
34	U/D	I	Vertical inversion
35	VGL	P	Gate OFF voltage
36	NC	-	No connection
37	NC	-	No connection
38	VGH	P	Gate ON voltage
39	LED+	P	LED anode
40	LED+	P	LED anode

I: input, P: Power

Note 1. Global reset pin. Active Low to enter Reset State. Normally pull high.

It's necessary to connecting with an RC delay circuit for stability.

(GRB delay VDD larger than 1ms)

Note 2. If LVDS input data is 6 bits, SELB must be set to High;
If LVDS input data is 8 bits, SELB must be set to Low.

Note 3. When L/R = "0", set right to left scan direction.
When L/R = "1", set left to right scan direction.
When U/D = "0", set top to bottom scan direction.

Note 4. U/D R/L Function description

SETTING OF SCAN CONTROL INPUT		SCANNING DIRECTION
U/D	L/R	
GND	DVDD	Up to down, left to right
DVDD	GND	Down to up, right to left
GND	GND	Up to down, right to left
DVDD	DVDD	Down to up, left to right

11. TIMING CHART

11.1 Parallel Timing Characteristic

11.1.1 DE Mode

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
DCLK frequency @Frame rate=60HZ	fclk	40.8	51.2	67.2	MHz
Horizontal display area	Thd	1024			DCLK
HSYNC period time	Th	1114	1344	1400	DCLK
HSYNC blanking	Thb+thfp	90	320	376	DCLK
Vertical display area	Tvd	600			H
VSYNC period time	Tv	610	635	800	H
VSYNC blanking	Tvb+tvfp	10	35	200	H

11.1.2 HV Mode (Horizontal input timing)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
DCLK frequency @Frame rate=60Hz	Fclk	44.9	51.2	63	MHz
Horizontal display area	Thd	1024			DCLK

1 Horizontal line	Th	1200	1344	1400	DCLK
HSYNC Pulse Width	Thpw	1	-	140	DCLK
HSYNC back porch	Thbp	160	160	160	DCLK
HSYNC front porch	Thfp	16	160	216	DCLK

11.1.3 HV Mode (Vertical input timing)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Vertical display area	Tvd	600			H
1 VSYNC period time	Tv	624	635	750	H
VSYNC Pulse Width	Tvpw	1	-	20	H
VSYNC back porch	Tvbp	23	23	23	H
VSYNC front porch	Tvfp	1	12	127	H

12. INSPECTION

Standard acceptance/rejection criteria for TFT module according to document available [here](#).

13. RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION	NOTE
1	High Temperature Storage	80°C/120 hours	A,B,C,D,E
2	Low Temperature Storage	-30°C/120 hours	
3	High Temperature Operating	70 °C /120 hours	
4	Low Temperature Operating	-20°C/120 hours	
5	High Temperature and High Humidity	+60°C, 90%RH, 120hrs	
6	Thermal Cycling Test (No operation)	-20°C/30 min ~ +70°C /30 min for a total 100 cycles, Start with cold temperature and end with high temperature.	A,B,C,D,E
7	Vibration Test	Sweep:10Hz~55Hz~10Hz 2G 2 hours for each direction of X. Y. Z. (6 hours for total)	A,B,C,D,E
8	Package Vibration Test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	A,B,C,D,E
9	Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces	A,B,C,D,E
10	ESD	Contact=+/-4KV, Air=+/-8KV,(R=330R,C=150pF), 1 sec,9point,10times/point;	A,B,C,D,E

Notes

- A. LCM each function is OK.
- B. LCM appearance inspection without abnormalities (Including scratch, damage, corrosion and serious deformation)
- C. LCM brightness above the Min. value of Spec.
- D. Luminance uniformity above the Min. value of Spec.
- E. Color chromaticity within tolerance range.

14. LEGAL INFORMATION

CE marking is usually obligatory only for a complete end product. Riverdi display modules are semi-finished goods which are used as inputs to become part of the finished products.

Therefore, Riverdi display modules are not CE marked.

This is not a standalone product. It was designed as an electronic component. It needs integration with a whole system to be fully functional.

Riverdi grants the guarantee for the proper operation of the goods for a period of 12 months from the date of possession of the goods. If in a consequence of this guaranteed execution the customer has received the defects-free item as replacement for the defective item, the effectiveness period of this guarantee shall start anew from the moment the customer receives the defects-free item.

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